AX251

- Supports Intel 8x251SB (ROM & ROMless)
- Future Derivatives Supported by Changing Pod
- Performance Analysis of User Software Code
- Sophisticated Trace Recording Using HLL Lines
- 256K Overlay Memory in 1K Segments
- 256K x 2 Coverage For Code and Data
- 768K Hardware Breakpoints
- Hardware Triggering: Sequencing and Counter Logic
- 32K by 80 Bits Real-Time Filtered Trace Buffer
- Supports Many C-251 Compilers

The AX251 is the latest 16-bit emulator design from Hitex for the Intel MCS® 251 microcontroller family. The Hitex designers incorporated many of the popular features of the AX51 and the MX51 into this new emulator. The Intel 8x251SB microcontroller is supported and future variants will be supported with new pods. The AX251 operates in genuine real-time and does not require resources from the target system which facilitates effective and productive debugging sessions for the designer.

The HiTOP user interface operates under Windows* 3.1, Windows NT and Windows 95. HiTOP is the same user interface as used on all Hitex products.

Keil, BSO/Tasking and PLC are some of the 251 compilers supported. HiTOP provides unrestricted High-Level-Language (HLL) debugging capabilities. HLL debugging as well as assembly language debugging is supported with all symbols and comments displayed. The Performance Analysis feature provides increased efficiency and control in the optimization of the user code.

Code coverage and data coverage of 256K each is useful to find unused code and uninitialized and unused data areas.

The hardware breakpoint system consists of separate 256K execution and 256K data access breakpoints. Execution breakpoints stop the emulation before executing the instruction where the breakpoint is set. Execution breakpoints can be set in ROM as well as writable memory.

The complex trigger conditions can be used to control trace recording and time measurement. The triggers can be combined logically in sequences. The trace information is recorded in real-time and can be displayed in either high-level language or assembler form. The trace buffer size is 32K by 80 bits and utilizes the exclusive Hitex Intelligent Lines Recording Method to efficiently use memory in a highly productive form for the user. A tag RAM-based filter mechanism is used to select only those areas of memory to be stored in the trace buffer.

MICROCONTROLLERS SUPPORTED:
8xC151Sx, 8xC251Sx, 82930A

DEVELOPMENT PLATFORMS:
DOS, Windows 3.1, Windows 95
Windows NT

AVAILABILITY:
Now

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